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(71) Applicant:

**SHARP KABUSHIKI KAISHA  
Osaka 545-8522 (JP)**

(72) Inventors:

- Hooper, Stewart Edward  
Kidlington, Oxon OX5 2JS (GB)

- Barnes, Jennifer Mary

Hampton, Middlesex TW12 1EP (GB)

- Heffernan, Jonathan

Oxford OX2 9BW (GB)

- Kean, Alistair Henderson

Stadhampton, Oxfordshire OX44 7TT (GB)

(74) Representative:

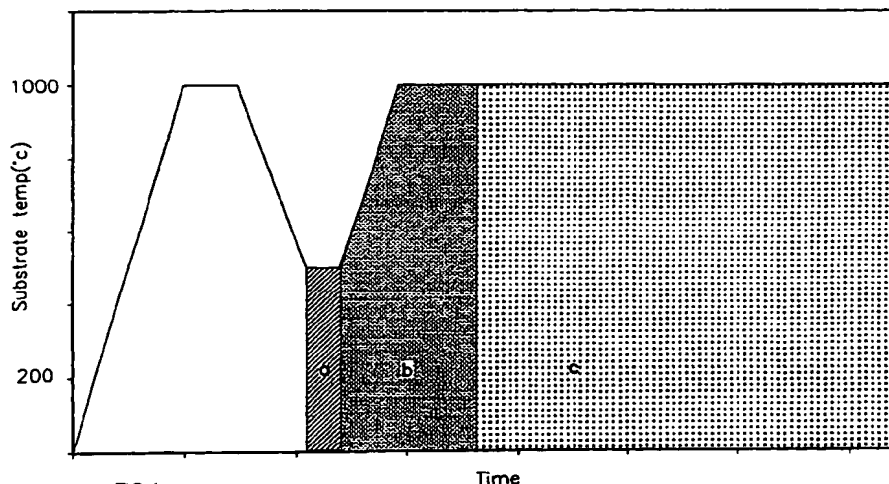
**Suckling, Andrew Michael et al**  
**Marks & Clerk**  
**4220 Nash Court**  
**Oxford Business Park South**  
**Oxford OX4 2RU (GB)**

**(54) MBE growth of group III-nitride semiconductor layers**

(57) A method of growing a nitride semiconductor layer, such as a GaN layer, by molecular beam epitaxy comprises the step of growing a GaAlN nucleation layer on a substrate by molecular beam epitaxy. The nucleation layer is annealed, and a nitride semiconductor layer is then grown over the nucleation layer by molecular beam epitaxy. The nitride semiconductor layer is grown at a V/III molar ratio of 100 or greater, and this enables a high substrate temperature to be used so that a good quality semiconductor layer is obtained. Ammonia gas

is supplied during the growth process, to provide the nitrogen required for the MBE growth process.

In one embodiment of the invention, the substrate temperature during the step of annealing the nucleation layer is in the range 950-1050°C and the substrate temperature during the growth of the nitride semiconductor layer is within the range 900-1050°C.

**FIG.1**

## Description

[0001] This invention relates to a molecular beam epitaxy (MBE) method for the epitaxial growth of Group III nitride semiconductor materials, such as GaN.

[0002] The epitaxial growth of Group III nitride semiconductor materials on a substrate can be effected by molecular beam epitaxy (MBE) or by chemical vapour deposition (CVD) which is sometimes known as Vapour Phase Epitaxy (VPE).

[0003] CVD (or VPE) takes place in an apparatus which is commonly at atmospheric pressure but sometimes at a slightly reduced pressure of typically about 10 kPa. Ammonia and the species providing one or more Group III elements to be used in epitaxial growth are supplied substantially parallel to the surface of a substrate upon which epitaxial growth is to take place, thus forming a boundary layer adjacent to and flowing across the substrate surface. It is in this gaseous boundary layer that decomposition to form nitrogen and the other elements to be epitaxially deposited takes place so that the epitaxial growth is driven by gas phase equilibria.

[0004] In contrast to CVD, MBE is carried out in a high vacuum environment. In the case of MBE as applied to the GaN system, an ultra-high vacuum (UHV) environment, typically around  $1 \times 10^{-3}$  Pa, is used. Ammonia or another nitrogen precursor is supplied to the MBE chamber by means of a supply conduit and species providing gallium and, possibly, indium and/or aluminium are supplied from appropriate sources within heated effusion cells fitted with controllable shutters to control the amounts of the species supplied into the MBE chamber during the epitaxial growth period. The shutter-control outlets from the effusion cells and the nitrogen supply conduit face the surface of the substrate upon which epitaxial growth is to take place. The ammonia and the species supplied from the effusion cells travel across the MBE chamber and reach the substrate where epitaxial growth takes place in a manner which is driven by the deposition kinetics.

[0005] GaN has a lattice constant of around 0.45nm. There is a lack of suitable substrates that are lattice-matched to GaN, so GaN is generally grown onto either a sapphire substrate or a silicon carbide substrate. Because of the lattice mis-match between GaN and sapphire or silicon carbide, it is necessary to provide a thin initial nucleation layer on the substrate in order to grow high quality GaNs on sapphire or silicon carbide.

[0006] Akasaki and Amano report, in "Japanese Journal of Applied Physics" Vol. 36, pp5393 - 5408 (1997), that a thin AlN layer, deposited at a low growth temperature, can be used as a nucleation layer to promote the growth of a GaN layer by metal organic chemical vapour deposition (MOCVD) process on a sapphire or silicon carbide substrate.

[0007] US Patent No. 5 290 393 discloses the use of a GaN nucleation layer, deposited at a low growth

temperature, for promoting the growth of a GaN layer using MOCVD.

[0008] The above prior art documents do not discuss the effect of thermal treatment of the nucleation layer on the properties of the subsequent GaN layer. Lin et al in "Applied Physics Letters" Vol. 68, pp3758 - 3760 (1996) and Sugiura et al in "Journal of Applied Physics" Vol. 82, p4877 — 4882 (1997) report that controlled annealing of the nucleation layer can improve the quality of the subsequent GaN layer. Both of these studies on the effect of annealing the nucleation layer were carried out for GaN layers grown by MOCVD.

[0009] At present, the majority of growth of high quality GaN layers is carried out using the MOCVD process. The MOCVD process allows good control of the growth of the nucleation layer and of the annealing of the nucleation layer. Furthermore, the MOCVD process allows growth to occur at a V/III molar ratio well in excess of 1000:1. (The V/III molar ratio is the molar ratio of the group V element to the Group III element during the growth process. A high V/III molar ratio is preferable, since this allows a higher substrate temperature to be used leading to a higher quality GaN layer.)

[0010] At present, growing high quality GaN layers by MBE is more difficult than growing such layers by MOCVD. The principal difficulty is in supplying sufficient nitrogen during the growth process; it is difficult to obtain a V/III molar ratio of 10:1 or greater. The two commonly used sources of nitrogen in the MBE growth of nitride layers are plasma excited molecular nitrogen or ammonia.

[0011] US Patent No. 5 385 862 and WO 92/16966 disclose a method of growing a single crystal GaN film on a sapphire substrate using MBE. In this method, the nitrogen used in the growth process is activated nitrogen obtained by exciting molecular nitrogen using a plasma source. In this method, the growth of the initial nucleation layer is restricted to a growth temperature of 400°C or lower, and the growth of the subsequent GaN layer is restricted to a growth temperature of lower than 900°C. This patent discloses a step of crystallising the GaN nucleation layer, but specifies no time period for this step. GaN layers grown by this method have electron mobilities at room temperature of less than  $100\text{cm}^2\text{V}^{-1}\text{s}^{-1}$ .

[0012] A method of growing GaN by MBE in which ammonia is used as the nitrogen source is reported by Yang et al in "Applied Physics Letters" Vol 67, pp1686 - 1688 (1995). In this prior art method, a GaN nucleation layer is grown using nitrogen obtained from plasma excited molecular nitrogen, and the nucleation layer is not annealed. The subsequent GaN semiconductor layer is grown at the low growth temperature of 750°C due to the low V/III molar ratio of the growth process. This method produces GaN layers having a room temperature mobility of less than  $100\text{cm}^2\text{V}^{-1}\text{s}^{-1}$ .

[0013] Further reports on the growth of GaN layers using MBE, with ammonia as the nitrogen source, have

been made by Grandjean et al in (1) "Journal of Applied Physics" Vol 83, pp1379 - 1383 (1998) and (2) "Applied Physics Letters" Vol 71, p240 - 242 (1997). Both of these documents report methods in which a GaN nucleation layer is used. The nucleation layer is annealed, although at the low temperature of 900°C owing to the low quantity of ammonia present during the annealing step. The subsequently grown GaN layer is grown at a low growth temperature of 830° C owing to the low V/III ratio obtained in the growth process. Grandjean et al (1) report a V/III ratio (atomic nitrogen to gallium) of 3-5:1 (equivalent to a molar ratio of about 60-100:1), whereas Grandjean et al (2) do not give a value for the V/III ratio. The methods reported in these two documents produce GaN layers having a room temperature electron mobility of less than  $150\text{cm}^2\text{V}^{-1}\text{s}^{-1}$ .

**[0014]** EP-A-0 607 435 discloses a method of growing a nitride semiconductor material by MBE. The method comprises growing a thin layer of an oriented polycrystalline nitride semiconductor material on a substrate, and subsequently growing a single crystal nitride semiconductor layer on the thin polycrystalline layer. The polycrystalline layer is not annealed, so that the thin layer will retain its polycrystalline structure when the single crystal layer is grown.

**[0015]** EP-A-0 497 350 relates to the growth of a GaN layer on a sapphire substrate by an MOCVD process. A GaAlN buffer layer is grown on the substrate, and a GaN layer is grown on the buffer layer. This document investigates how changes in the composition of the buffer layer affect the quality of the epitaxial layer.

**[0016]** It can therefore be seen that it is desirable to make possible a method for growing high mobility GaN layers using the MBE process. In order to do this, it is necessary to increase the growth temperatures of the GaN layer. The growth temperature is important in order to achieve polycrystalline material and good surface coverage. The annealing temperature is important in order to crystallise and flatten the nucleation layer.

**[0017]** A first aspect of the present invention provides a method of growing a nitride semiconductor layer by molecular beam epitaxy, the method comprising the steps of:

- (a) disposing a substrate in a vacuum chamber;
  - (b) growing a nucleation layer of  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  ( $0 \leq x \leq 1$ ) on the substrate by molecular beam epitaxy;
  - (c) annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer; and
  - (d) growing a nitride semiconductor layer on the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer by molecular beam epitaxy at a V/III molar ratio within the vacuum chamber of 100:1 or greater;
- wherein the method further comprises supplying ammonia gas to the vacuum chamber in step (b) and step (d).

**[0018]** Because the growth of the GaN layer is carried out at a V/III molar ratio within the vacuum chamber

of 100:1 or greater in the present invention, the growth process can be carried out at a higher temperature. In consequence, the present invention produces a higher quality nitride semiconductor layer than do the prior art methods. The present invention can produce, for example a GaN layer having an electron mobility at room temperature greater than  $250\text{cm}^2\text{V}^{-1}\text{s}^{-1}$ . This mobility compares favourably with the mobilities obtained by the MOCVD method. Moreover, supplying ammonia gas to the vacuum chamber in step (b) and step (d) is a convenient way of providing the nitrogen required for the MBE growth process, and eliminates the need to generate a nitrogen or ammonia plasma to provide activated nitrogen.

**[0019]** Step (d) may comprise growing the nitride layer at a V/III molar ratio within the vacuum chamber of 500:1 or greater or at a V/III molar ratio within the vacuum chamber of 1000:1 or greater. Step (d) may comprise growing the nitride layer at a V/III molar ratio within the vacuum chamber in the range 100:1 — 10,000:1.

**[0020]** Step (b) may comprise growing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer at a V/III molar ratio within the vacuum chamber of at least 100:1. It may comprise growing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer at a V/III molar ratio within the vacuum chamber in the range 100:1 — 10,000:1.

**[0021]** The thickness of the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer grown in step (b) may be within the range 5 - 200nm.

**[0022]** The substrate temperature during step (b) may be within the range 400 - 600°C.

**[0023]** Step (c) may comprise supplying ammonia gas to the vacuum chamber during the step of annealing of the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer.

**[0024]** The substrate temperature during the step of annealing of the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer may be 950-1050°C.

**[0025]** The duration of the step of annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer may be between 0 and 90 minutes.

**[0026]** The duration of the step of annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer may be approximately 45 minutes and the thickness of the nucleation layer may be 20nm. Alternatively, the duration of the step of annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer may be approximately 15 minutes and the thickness of the nucleation layer may be 70nm.

**[0027]** The substrate temperature during step (d) may be within the range 900 - 1050°C.

**[0028]** A second aspect of the present invention provides a nitride semiconductor layer grown by a method defined above.

**[0029]** A third aspect of the present invention provides a semiconductor device comprising a nitride semiconductor layer defined above.

**[0030]** Preferred embodiments of the present invention will now be described by way of illustrative example with reference to the accompanying figures in which:

Figure 1 is a schematic illustration of the substrate temperature against time during a growth process according to one embodiment of the present invention;

Figure 2 is cross-sectional transmission electron micrograph of a layer of GaN grown using a method of the present invention;

Figure 3 shows photoluminescence spectra, at four different temperatures, of a non-intentionally doped GaN layer grown using a method of the present invention;

Figure 4 shows the effect of the thickness of a GaN nucleation layer on the carrier density and mobility of a subsequent GaN layer grown by a MBE growth method of the present invention;

Figure 5 shows the effect of the duration of the annealing step of a GaN nucleation layer on the carrier density and mobility of a subsequent GaN layer grown using a MBE method of the present invention; and

Figure 6 is a schematic diagram of an MBE chamber suitable for putting the method of the invention into effect.

the substrate temperature is increased to a temperature between 950 and 1,050°C. The temperature ramp rate must be chosen to prevent undue thermal stress within the substrate, and also between the substrate and the nucleation layer (which have different coefficients of expansion), and a temperature ramp rate in the range 10-120°C per minute is suitable. The temperature is then maintained at this value and the nucleation layer is annealed, preferably under an ammonia flux. This stage is illustrated as (b) in Figure 2.

[0038] The duration of the annealing step is preferably between 0 and 90 minutes.

[0039] A GaN nucleation layer grown at a temperature in the range 400 - 600°C will have a polycrystalline structure. When the nucleation layer is annealed, its crystal structure will change from polycrystalline to monocrystalline.

[0040] Once the annealing step has been concluded, a GaN semiconductor layer is grown over the nucleation layer. The substrate temperature during this step is kept within the range 900-1,050°C. The rates of supply of the ammonia gas and elemental gallium are chosen to give a V/III molar ratio of at least 100:1 in the reaction chamber, and the GaN layer is grown by molecular beam epitaxy. Although the V/III molar ratio in the growth chamber can have any value within the range between 100:1 to 10,000:1, since the highest possible substrate temperature during the growth step increases as the V/III molar ratio within the growth chamber increases, the V/III molar ratio in the growth chamber is preferably greater than 500:1 and more preferably greater than 1,000:1.

[0041] In the present invention, ammonia gas is supplied to the growth chamber of the MBE apparatus to provide the nitrogen required for the growth of the GaN nucleation layer and the GaN semiconductor layer. The present invention does not require the generation of activated nitrogen, for example using a plasma source, to provide the nitrogen for the MBE growth process.

[0042] Figure 2 is a cross-sectional transmission electron micrograph of a GaN layer grown by a molecular beam epitaxy method of the present invention. Figure 2 shows the cross section of a non-intentionally doped GaN layer having a thickness of 4µm. It will be noted that the upper 3µm of the GaN layer are relatively free of threading dislocations. The density of threading dislocations in the top 3µm of the GaN layer of Figure 2 is approximately  $8 \times 10^8 \text{ cm}^{-2}$ . In contrast, a GaN layer grown by a conventional MBE method would be expected to have a density of threading dislocations of at least  $5 \times 10^9 \text{ cm}^{-2}$ , according to Grandjean et al (1) above. A typical density of threading dislocations in GaN grown by MOCVD is between  $10^8$  to  $10^{10} \text{ cm}^{-2}$ , according to Lester et al "Applied Physics Letters" Vol 66, pp1249-1251 (1995).

[0043] Room temperature Hall effect measurements of non intentionally doped GaN films grown using a molecular beam epitaxy method of the present inven-

tion typically yield carrier concentrations (n-type) less than  $5 \times 10^{17} \text{cm}^{-3}$ , and also yield electron mobilities greater than  $250 \text{cm}^2 \text{V}^{-1} \text{s}^{-1}$ . These results compare favourably with values for typical MOCVD GaN material as reported by Akasaki and Amano (above).

**[0044]** Figure 3 illustrates the photoluminescence spectrum of non-intentionally doped GaN layer grown using a molecular beam epitaxy method of the present invention. The spectrum is shown at four different temperatures. It will be seen that the spectrum is dominated by near band edge emission, including a free exciton peak. Such a photoluminescence spectrum is indicative of a high quality GaN layer.

**[0045]** Figure 4 illustrates the effect of the thickness of the GaN nucleation layer on the carrier concentration and carrier mobility of the subsequent GaN layer. The duration of the annealing step is 15 minutes for all thicknesses of the buffer layer. For this annealing time, a thickness of 70nm is preferable for the nucleation layer, as this provides the highest mobility and the lowest carrier concentration.

**[0046]** Figure 5 illustrates the effect of the annealing time on the carrier mobility and carrier density of the subsequent GaN layer. This data was obtained with a nucleation layer having a thickness of 20nm and, for this thickness of nucleation layer, an annealing period of 45 minutes is found to obtain the highest mobility and the lowest carrier concentration.

**[0047]** The use of a high V/III molar ratio within the growth chamber during the growth process of the present invention allows a nitride semiconductor layer to be grown at temperatures well above those used in prior art MBE methods. This leads to improved material quality. Compared to a MOCVD process, the MBE growth process of the present invention requires at least one thousand times less ammonia gas.

**[0048]** The present invention requires a MBE growth apparatus that can achieve a V/III molar ratio of more than 100:1 within the growth chamber during the growth process. Such high V/III molar ratios can be achieved, for example, in a MBE growth apparatus in which ammonia gas is introduced into the growth chamber through a conduit whose outlet end is placed as close to the substrate as possible without radiative heat from the substrate causing excessive local heating of the outlet of the supply conduit. The elemental gallium is introduced into the growth chamber using a conventional effusion cell. Further effusion cells can be used to supply aluminium and/or indium and/or an elemental dopant for incorporation into the epitaxial growth material as necessary.

**[0049]** Figure 6 is a schematic view of an apparatus suitable for the growth of a nitride semiconductor material by molecular beam epitaxy according to a method of the present invention. The apparatus comprises a vacuum chamber 10 in which is disposed a heated support 12 arranged to support and heat a substrate S. The vacuum chamber 10 is connected with an ultra-high vac-

uum pump 14 via an exhaust conduit 16 which extends into the vacuum chamber 10. The inner end of the exhaust conduit 16 defines a vacuum outlet 18 of the vacuum chamber 10. The vacuum outlet 18 is disposed adjacent to the substrate support 12. In the MBE apparatus of Figure 6 MBE growth occurs in the vacuum chamber 10, which thus functions as the growth chamber of the apparatus.

**[0050]** The vacuum chamber 10 is further provided with a first supply conduit 20 which extends into the vacuum chamber so that an outlet 22 of the first supply conduit 20 is adjacent to and faces the surface of the substrate S upon which epitaxial growth is to take place. The first supply conduit 20 can be adjustably mounted relative to the chamber so that the relatively small distance between the outlet 22 of the first supply conduit 20 and the epitaxial growth surface of the substrate S can be varied during the epitaxial growth process. The longitudinal axis of the first supply conduit 20 is substantially perpendicular to the plane of epitaxial growth.

**[0051]** The first supply conduit 20 is used to supply ammonia which is the precursor of the nitrogen required in the epitaxial growth process. Because the outlet 22 of the first supply conduit 20 is positioned relatively close to the substrate S, a relatively high ammonia vapour pressure is localised at the surface of the epitaxially growing material while still enabling an ultra-high vacuum environment within the vacuum chamber 10 to be achieved by the pump 14. The high ammonia vapour pressure enables a high V/III molar ratio to be realised within the vacuum chamber 10 during the growth process.

**[0052]** The apparatus further comprises independently operable, shutter-controlled effusion cells 24, 26 (two such cells are shown in Figure 6) which contain sources of elemental gallium and another element (for example, aluminium, indium or a dopant) which may be required during the epitaxial growth process. The effusion cells 24 and 26 are conventionally positioned and define second and further supply conduits respectively.

**[0053]** A MBE apparatus of the type described above is described in European Patent Application No. 98301842.5, the contents of which are hereby incorporated by reference. It should be noted, however, that the present invention is not limited to a MBE apparatus of the type described in European Patent Application No. 98301842.5/ 0 864 672, but can be carried out in any MBE growth apparatus that can provide the required V/III molar ratio within the growth chamber.

**[0054]** In the embodiment described above, the nucleation layer is a GaN layer. The nucleation layer is not limited to this, however, and can be formed of  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  where  $0 \leq x \leq 1$ .

**[0055]** Although the embodiment described above relates to the growth of a GaN layer, the invention is not limited to this, but can be used for the growth of other nitride semiconductor layers. For example, GaAlN and GaInN can also be grown by a method of the present

invention.

**[0056]** In the embodiment described above, the GaN layer that is grown is not intentionally doped. It is, however, possible to dope the GaN layer by introducing a suitable dopant during the MBE growth process, in a manner which is well known per se. Suitable dopants include silicon, tin, germanium, magnesium, calcium, carbon, beryllium and indium.

**[0057]** Once the nitride semiconductor layer has been grown by a molecular beam epitaxy method of the present invention, further semiconductor layers can be grown so as to produce an (Al,Ga,In)N electronic or opto-electronic device. For example, a GaN layer grown by a method of the present invention can be used as the basis for a laser device emitting light in the wavelength range 380-450 nm.

#### Claims

1. A method of growing a nitride semiconductor layer by molecular beam epitaxy, the method comprising the steps of:
  - a) disposing a substrate (S) in a vacuum chamber (10);
  - b) growing a layer of  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  ( $0 \leq x \leq 1$ ) on the substrate (S) by molecular beam epitaxy;
  - c) annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer; and
  - d) growing a nitride semiconductor layer on the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer by molecular beam epitaxy at a V/III molar ratio within the vacuum chamber of 100:1 or greater;
 

wherein the method further comprises supplying ammonia gas to the vacuum chamber in step (b) and step (d).
2. A method as claimed in claim 1 wherein step (d) comprises growing the nitride layer at a V/III molar ratio within the vacuum chamber of 500:1 or greater.
3. A method as claimed in claim 1 wherein step (d) comprises growing the nitride layer at a V/III molar ratio within the vacuum chamber of 1000:1 or greater.
4. A method as claimed in claim 1 wherein step (d) comprises growing the nitride layer at a V/III molar ratio within the vacuum chamber in the range 100:1 — 10,000:1.
5. A method as claimed in any preceding claim wherein step (b) comprises growing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer at a V/III molar ratio within the vacuum chamber of at least 100:1 in step (b).
6. A method as claimed in claim 5 wherein step (b) comprises growing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer at a V/III molar ratio within the vacuum chamber in the range 100:1 — 10,000:1 in step (b).
7. A method as claimed in any preceding claim wherein the thickness of the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer grown in step (b) is within the range 5 - 200nm.
8. A method as claimed in any preceding claim wherein step (b) comprises growing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer at a substrate temperature within the range 400 - 600°C.
9. A method as claimed in any preceding claim, further comprising supplying ammonia gas to the vacuum chamber (10) during the step of annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer.
10. A method as claimed in any preceding claim wherein the substrate temperature during the step of annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer is 950 - 1050°C.
11. A method as claimed in any preceding claim wherein the duration of the step of annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer is between 0 and 90 minutes.
12. A method as claimed in claim 11 wherein the duration of the step of annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer is approximately 45 minutes and the thickness of the nucleation layer is 20nm.
13. A method as claimed in claim 11 wherein the duration of the step of annealing the  $\text{Ga}_x\text{Al}_{1-x}\text{N}$  layer is approximately 15 minutes and the thickness of the nucleation layer is 70nm.
14. A method as claimed in any preceding claim wherein the substrate temperature during step (d) is within the range 900 - 1050°C.
15. A nitride semiconductor layer grown by a method defined in any of claims 1 to 14.
16. A semiconductor device comprising a nitride semiconductor layer as claimed in claim 15.

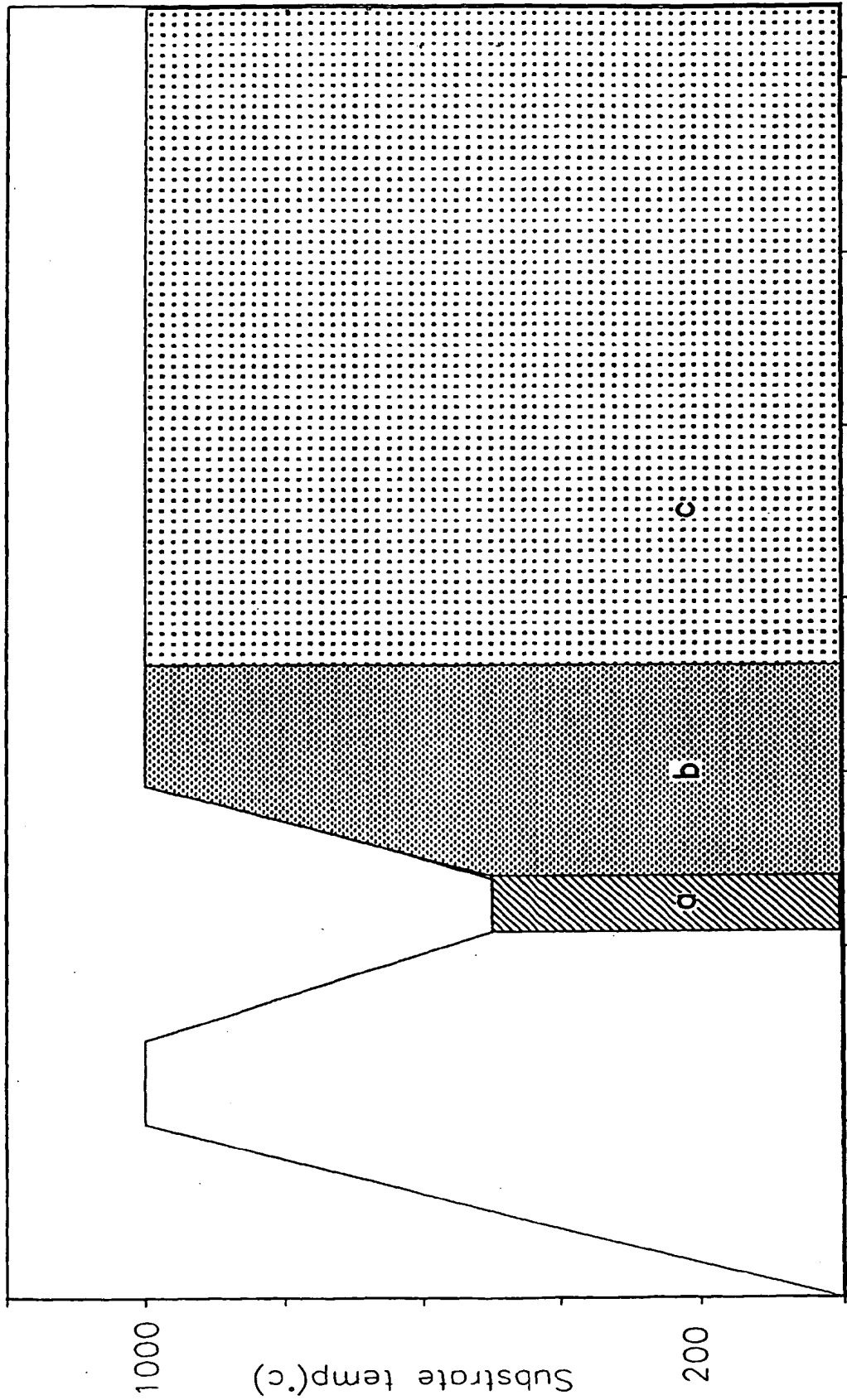


FIG.1

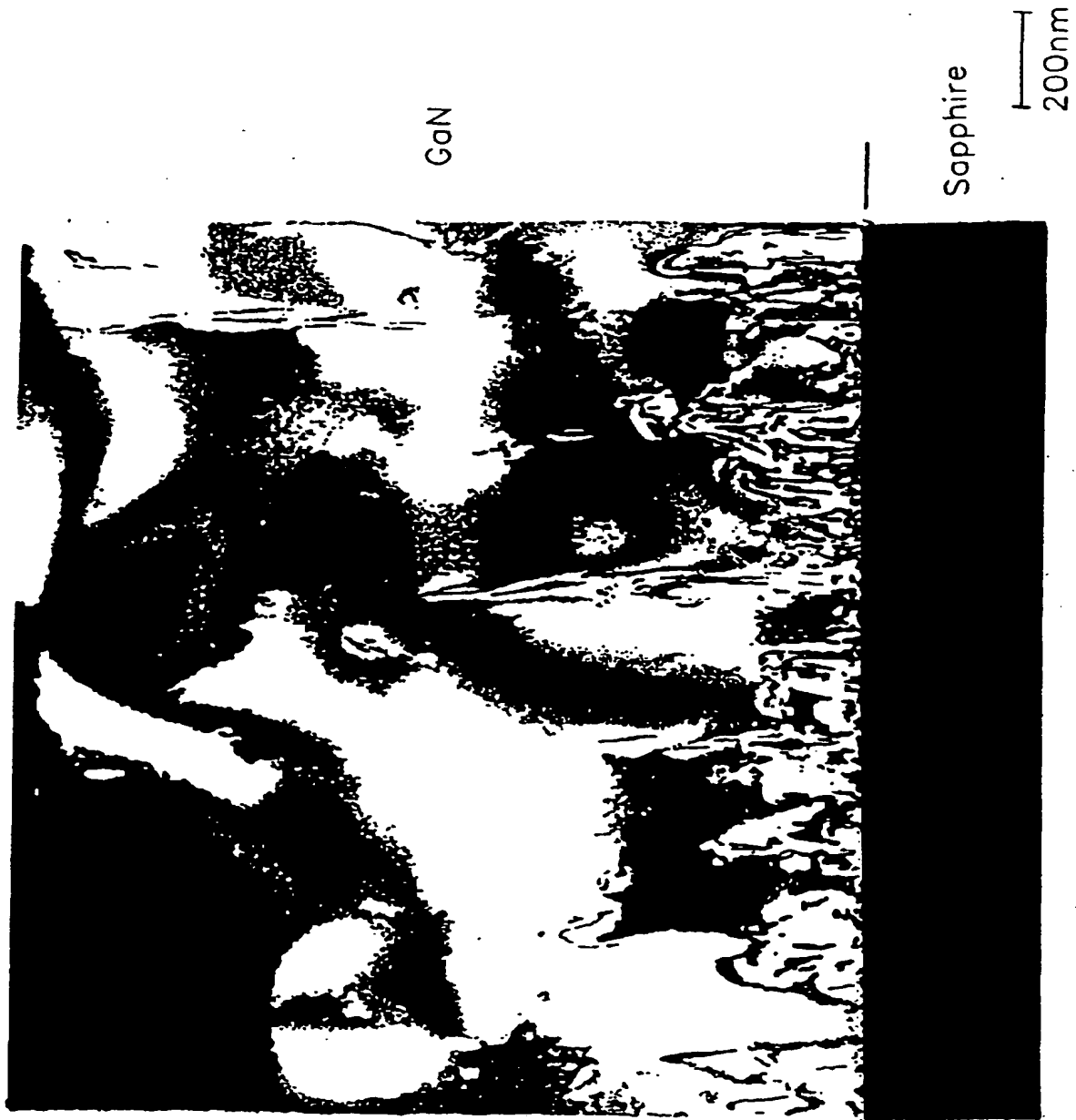
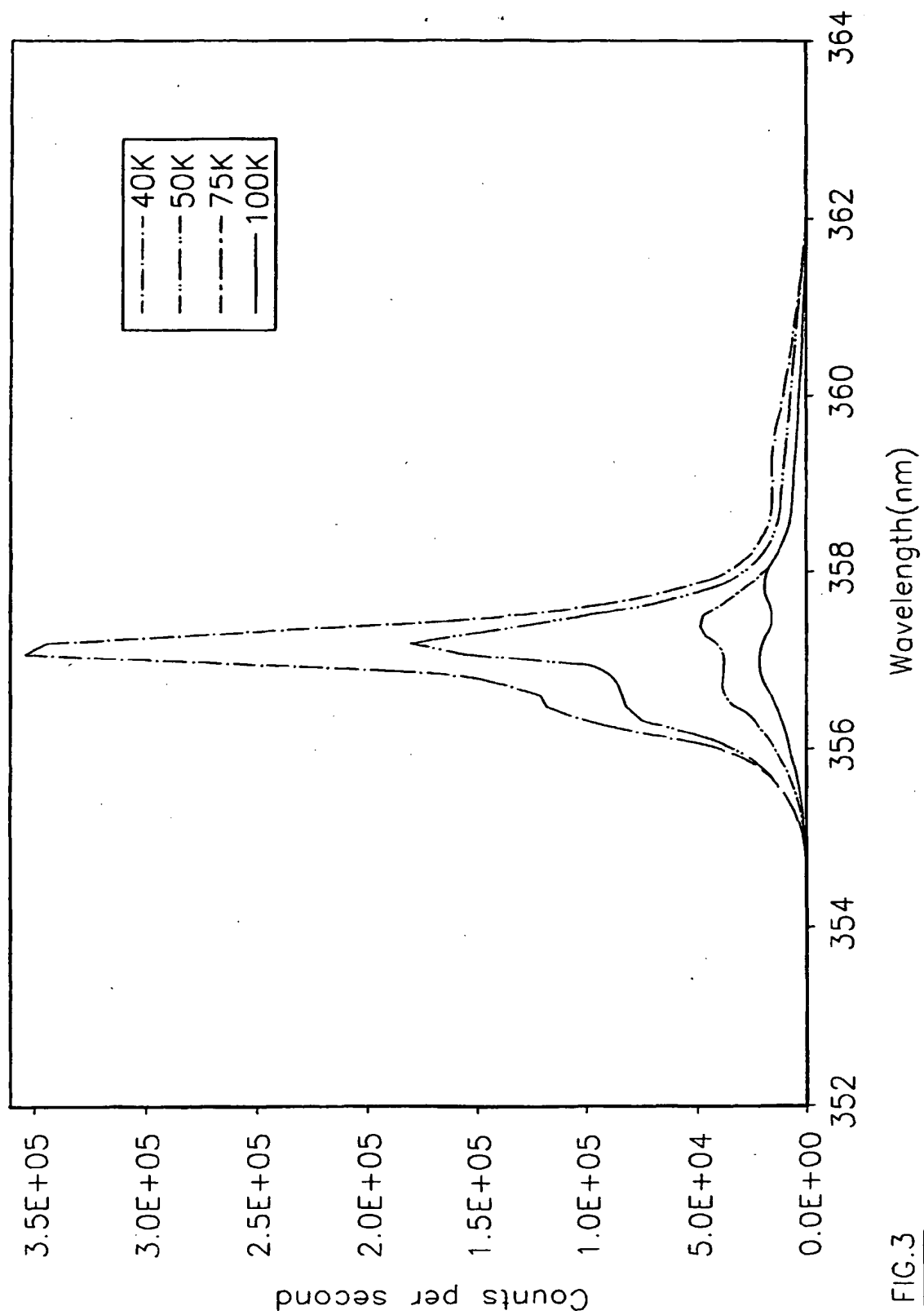
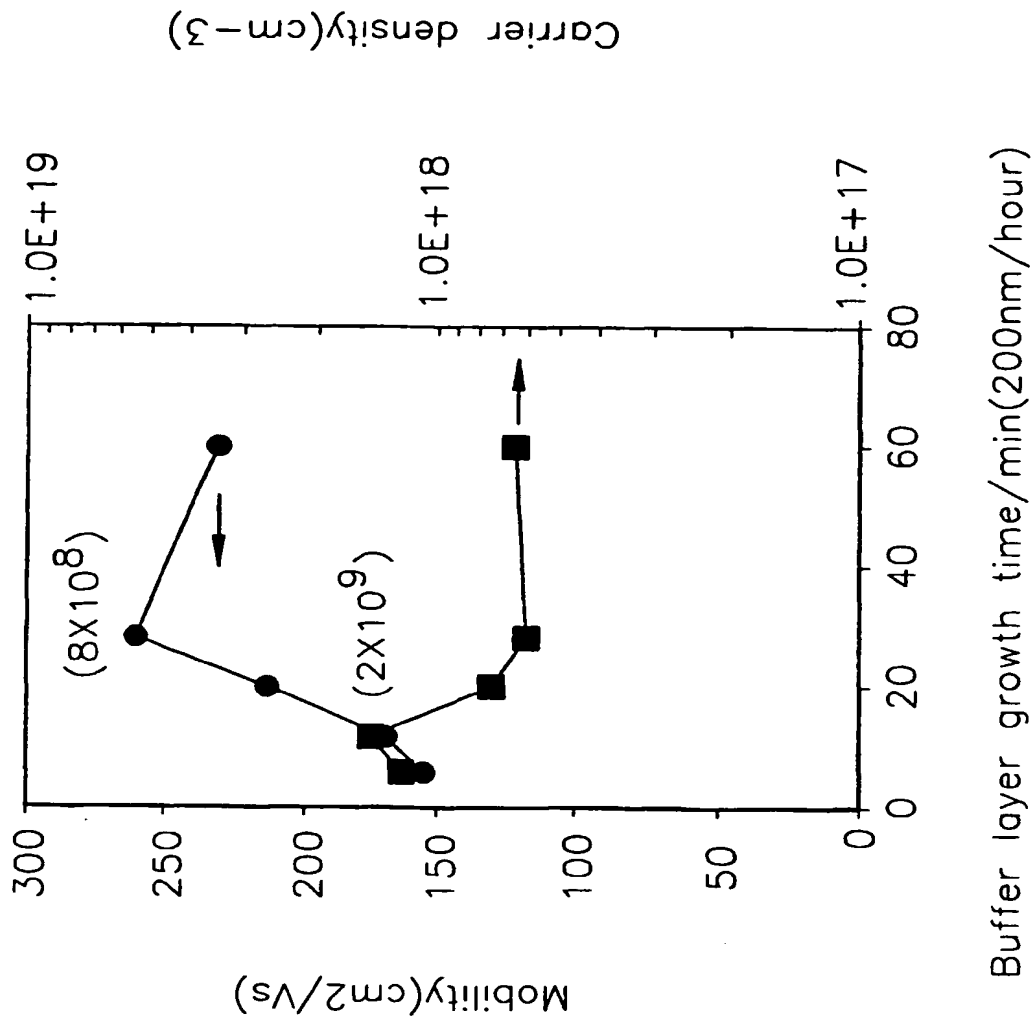
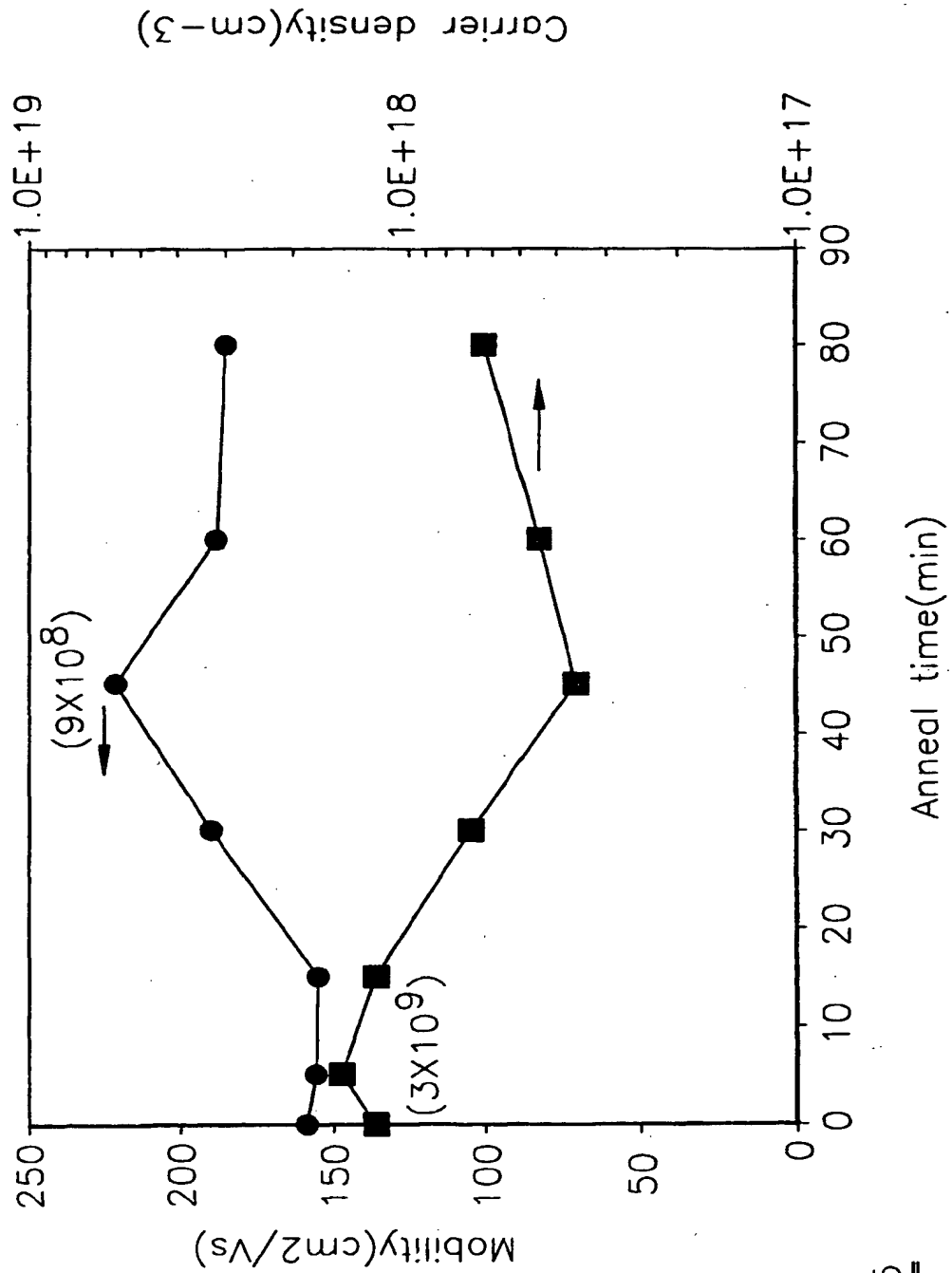


FIG. 2



FIG.3

FIG. 4

FIG.5

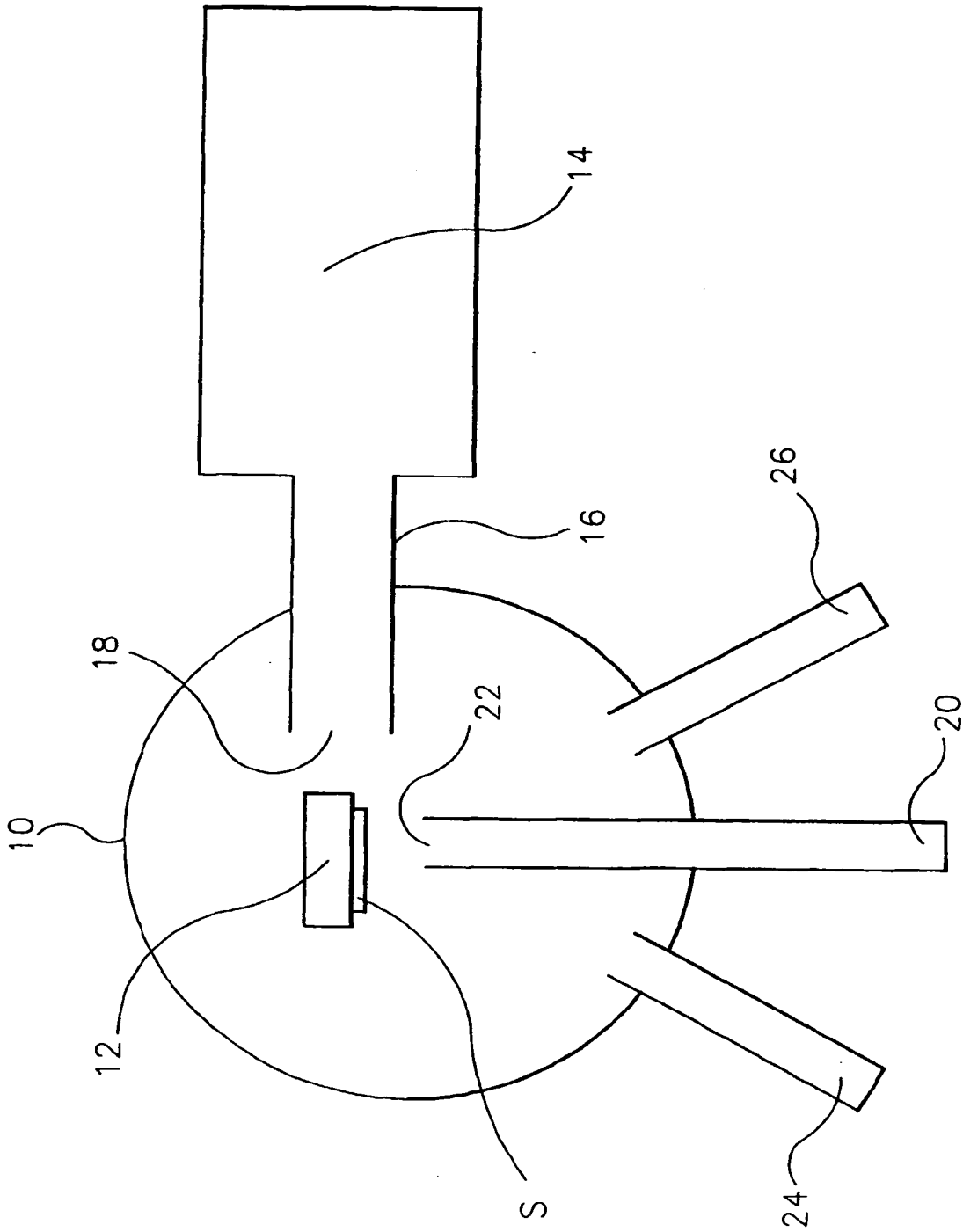
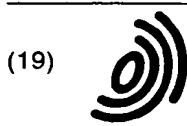


FIG. 6



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- **Barnes, Jennifer Mary**  
Hampton, Middlesex TW12 1EP (GB)
- **Heffernan, Jonathan**  
Oxford OX2 9BW (GB)
- **Kean, Alistair Henderson**  
Stadhampton, Oxfordshire OX44 7TT (GB)

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(71) Applicant: **SHARP KABUSHIKI KAISHA**  
**Osaka 545-8522 (JP)**

(74) Representative: **Suckling, Andrew Michael et al**  
**Marks & Clerk**  
**4220 Nash Court**  
**Oxford Business Park South**  
**Oxford OX4 2RU (GB)**

(72) Inventors:  
• **Hooper, Stewart Edward**  
**Kidlington, Oxon OX5 2JS (GB)**

(54) **MBE growth of group III-nitride semiconductor layers**

(57) A method of growing a nitride semiconductor layer, such as a GaN layer, by molecular beam epitaxy comprises the step of growing a GaAlN nucleation layer on a substrate by molecular beam epitaxy. The nucleation layer is annealed, and a nitride semiconductor layer is then grown over the nucleation layer by molecular beam epitaxy. The nitride semiconductor layer is grown at a V/III molar ratio of 100 or greater, and this enables

a high substrate temperature to be used so that a good quality semiconductor layer is obtained. Ammonia gas is supplied during the growth process, to provide the nitrogen required for the MBE growth process.

In one embodiment of the invention, the substrate temperature during the step of annealing the nucleation layer is in the range 950-1050°C and the substrate temperature during the growth of the nitride semiconductor layer is within the range 900-1050°C.

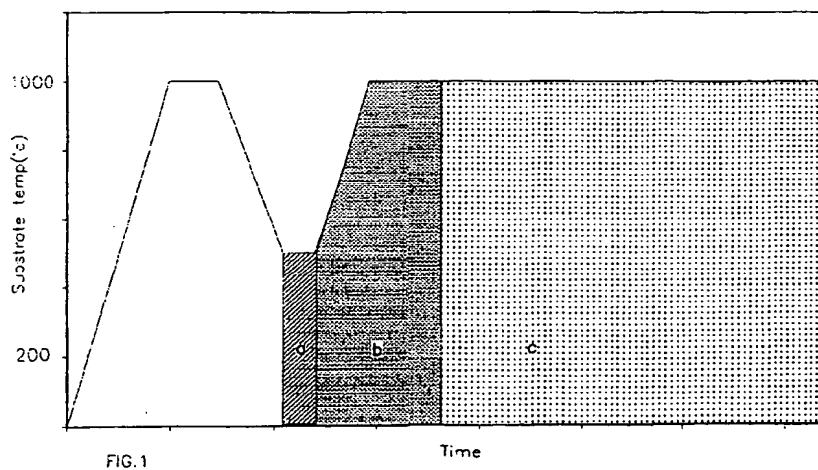


FIG. 1

Time



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Application Number  
EP 00 30 4900

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	BRIOT O ET AL: "Influence of the V/III molar ratio on the structural and electronic properties of MOVPE grown GaN" SOLID STATE ELECTRONICS, ELSEVIER SCIENCE PUBLISHERS, BARKING, GB, vol. 41, no. 2, 1 February 1997 (1997-02-01), pages 315-317, XP004033832 ISSN: 0038-1101	15,16	H01L21/203 C30B23/02 H01L21/205 C30B25/02
Y	* the whole document *	1-4, 7-11,14	
Y	WO 98 47170 A (KOZAKI TOKUYA ;CHOCHO KAZUYUKI (JP); IWASA NARUHITO (JP); KIYOKU H) 22 October 1998 (1998-10-22) * abstract * -& EP 0 942 459 A (NICHIA CHEMICAL INDUSTRIES, LTD.) 15 September 1999 (1999-09-15) * column 12, line 15 - column 14, line 43 *	1-4, 7-11,14	
			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
X	GRANDJEAN N ET AL: "Molecular beam epitaxy growth of nitride materials" MATERIALS SCIENCE AND ENGINEERING B, ELSEVIER SEQUOIA, LAUSANNE, CH, vol. 59, no. 1-3, 6 May 1999 (1999-05-06), pages 39-46, XP004173201 ISSN: 0921-5107 * the whole document *	1-16	H01L C30B
D,X	EP 0 497 350 A (NICHIA KAGAKU KOGYO KK) 5 August 1992 (1992-08-05) * the whole document *	15,16	
A	EP 0 285 358 A (CANON KK) 5 October 1988 (1988-10-05) * figures 16A-16I; example 6 *	1-16	
	-/--		
The present search report has been drawn up for all claims			
Place of search <b>THE HAGUE</b>		Date of completion of the search <b>19 March 2002</b>	Examiner <b>Nesso, S</b>
CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document		T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date D: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document	

EPO FORM 1503 03 82 (P&C01)



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# EUROPEAN SEARCH REPORT

Application Number  
EP 00 30 4900

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
A	MESRINE M ET AL: "EFFICIENCY OF NH3 AS NITROGEN SOURCE FOR GAN MOLECULAR BEAM EPITAXY" APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, vol. 72, no. 3, 19 January 1998 (1998-01-19), pages 350-352, XP000737388 ISSN: 0003-6951 * the whole document *	1-16	
P,X	ALEXEEV, A. N. ET AL.: "The growth rate evolution versus substrate temperature and V/III ratio during GaN MBE using ammonia" MRS INTERNET JOURNAL NITRIDE SEMICONDUCTOR RESEARCH, vol. 4, no. 6, 1999, pages 1-5, XP001059813 * the whole document *	1,2,4,8,9,11,14-16	
			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
The present search report has been drawn up for all claims			
Place of search <b>THE HAGUE</b>		Date of completion of the search <b>19 March 2002</b>	Examiner <b>Nesso, S</b>
<p><b>CATEGORY OF CITED DOCUMENTS</b></p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons &amp; : member of the same patent family, corresponding document</p>			

EP FORM 1503 03 82 (PAC01)

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ON EUROPEAN PATENT APPLICATION NO.**

EP 00 30 4900

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on  
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19-03-2002

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
WO 9847170	A	22-10-1998	CA	2258080 A1	22-10-1998
			EP	0942459 A1	15-09-1999
			JP	11191657 A	13-07-1999
			WO	9847170 A1	22-10-1998
			TW	406445 B	21-09-2000
			US	6153010 A	28-11-2000
			JP	11191637 A	13-07-1999
			JP	11191659 A	13-07-1999
			JP	11219910 A	10-08-1999
EP 0497350	A	05-08-1992	JP	4297023 A	21-10-1992
			JP	8008217 B	29-01-1996
			DE	69203736 D1	07-09-1995
			DE	69203736 T2	18-04-1996
			EP	0497350 A1	05-08-1992
			KR	9506968 B1	26-06-1995
			US	5290393 A	01-03-1994
EP 0285358	A	05-10-1988	JP	2579931 B2	12-02-1997
			JP	63239988 A	05-10-1988
			CA	1321121 A1	10-08-1993
			EP	0285358 A2	05-10-1988
			US	5010033 A	23-04-1991
			US	5304820 A	19-04-1994
			JP	1007516 A	11-01-1989

EPD FORM P0458

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82



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